

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Volker HÄRLE et al.

Serial No.: 10/566,521

Filed: September 25, 2006

For: Method for the Production of a Plurality of
Opto-Electronic Semiconductor Chips and Opto-
Electronic Semiconductor Chip

Examiner: Campbell, Shaun M.

Group Art: 2829

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

S I R:

In compliance with the duty of disclosure under 37 C.F.R. §1.56 and in accordance with the practice under 37 C.F.R. §§1.97 and 1.98, the Examiner's attention is directed to the documents listed on the enclosed form PTO/SB/08A. Copies of the listed non-U.S. patent documents are also enclosed.

JP 08-213712 corresponds to US 5,677,924.

U.S. application no. 10/566,521 has been previously mentioned as being related to the present application. The documents submitted herewith were cited in a Japanese application corresponding to the related application. An English translation of the Japanese Office Action is also enclosed.

In accordance with 37 C.F.R §§1.97(g) and (h), the filing of this Information Disclosure Statement should not be construed as a representation that a search has been made or that information cited is, or is considered to be, material to patentability as defined in §1.56(b),

or that any cited document listed or attached is (or constitutes) prior art. Unless otherwise indicated, the date of publication indicated for an item is taken from the face of the item and Applicant reserves the right to prove that the date of publication is in fact different.

This information is being submitted after issuance of a Notice of Allowance, but before payment of the Issue Fee. Pursuant to 37 C.F.R. §1.97 (i), please place this IDS in the file of the subject matter.

It is respectfully requested that the above information be considered by the Examiner and that a copy of the enclosed form PTO/SB/08A be returned indicating that such information has been considered.

Respectfully submitted,
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